

PATENT NUMBER

11036 U.S. PTO
09/922536

09/922536

08/03/01

| Class | Subclass |
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| ISSUE CLASSIFICATION | |

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A

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| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
| 09/922536 | | 365 | | 2818 | I |

APPLICANTS

Kai Yang
John Wang
Unsoon Kim

TITLE

Self-aligned gate formation using polysilicon polish with peripheral protective layer

PTO-2040
12/99

| ISSUING CLASSIFICATION | | | | | | | | | | | | |
|------------------------------|--|----------|--|--------------------|-----------------------------------|--|--|--|--|--|--|--|
| ORIGINAL | | | | CROSS REFERENCE(S) | | | | | | | | |
| CLASS | | SUBCLASS | | CLASS | SUBCLASS (ONE SUBCLASS PER BLOCK) | | | | | | | |
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| INTERNATIONAL CLASSIFICATION | | | | | | | | | | | | |
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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | NOTICE OF ALLOWANCE MAILED | |
| | _____ (Primary Examiner) (Date) | | ISSUE FEE | |
| Amount Due | | | Date Paid | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____ | _____ (Primary Examiner) (Date) | | ISSUE BATCH NUMBER | |
| <input type="checkbox"/> The terminal ____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | | |

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